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IBM Docket No. FR920000082US1

GP-2661\$

In the United States Patent and Trademark Office

Transmittal of
PRELIMINARY AMENDMENT

Date: February 19, 2002

In re Application of: A. Benayoun et al.

Serial Number: 10/043,028

Filed: January 9, 2002

For: Expandable Self-Route Multi-Memory Packet Switch with a
Configurable Multicast Mechanism

Art Unit: 2661

Examiner:

No additional fee is required.

X The fee due for this response is calculated below.

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| Claims Remaining After Amendment | | Highest Number Already Paid For* | | | Present Extra | | Rate | | Additional Fee |
|-------------------------------------|---|-------------------------------------|----|---|------------------|---|----------|---|-------------------|
| Total | 42 | - | 39 | = | 3 | @ | \$18.00 | = | \$54.00 |
| Independent | 2 | - | 3 | = | 0 | @ | \$84.00 | = | \$0.00 |
| | First Presentation of Multiple Dependent Claims | | | | | | \$280.00 | = | \$0.00 |
| TOTAL | | | | | | | | | \$54.00 |

*Total claims previously paid for must be 20 or more. Independent claims previously paid for must be 3 or more.

Please charge Deposit Account 09-1990 for the TOTAL set forth above. The Commissioner is authorized to charge payment of any additional fees required under 37 CFR §1.116 and 37 CFR §1.117 or to credit any overpayment to the designated Deposit Account. A duplicate copy of this sheet is enclosed.

Certificate of Mailing Under 37 CFR 1.8(a)

Date: FEBRUARY 19, 2002

I hereby certify that I am depositing the enclosed or attached correspondence with the U.S. Postal Service on the above date, as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

Karen Orzechowski

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BY:

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Date: February 19, 2002

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PATENT

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CERTIFICATE OF MAILING (37 C.F.R. 1.8(a))

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hmk
3-26-02

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Examiner: Unknown

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

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In the Claims:

Add the following new Claims 17-19:

17. A data transmission system including:
a switch module having at least two crosspoints;
a memory operatively coupled to each one of the at least two crosspoints;

Serial Number 10/043028

- 1 -

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